Spatial profiles of interelectrode electron density in direct current superposed dual-frequency capacitively coupled plasmas

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We present experimentally determined spatial profiles of the interelectrode electron density (n<sub>e</sub>) in dual-frequency capacitively coupled plasmas in which the negative direct current (dc) bias voltage (V<sub>dc</sub>) is superposed; in the experiment, 13 MHz (P<sub>low</sub>) was applied to the lower electrode and 60 MHz (P<sub>high</sub>) to the upper electrode. The bulk n<sub>e</sub> increased substantially with increases in the external power, P<sub>high</sub>, P<sub>low</sub>, and with increases in V<sub>dc</sub>. When P<sub>low</sub> was insufficient, the bulk n<sub>e</sub> decreased as the V<sub>dc</sub> bias increased. The bulk n<sub>e</sub> increased due to its dependence on V<sub>dc</sub>, especially for  $|V_{dc}| >$ 500 V. This may correspond to the sheath voltages (V<sub>s</sub>) of the lower electrode. The n<sub>e</sub> values in front of the upper electrode were coupled with the V<sub>dc</sub>: the V<sub>dc</sub> dependence first decreased and then increased. The dc currents (I<sub>dc</sub>) of the upper electrode were collected when a large P<sub>low</sub> was applied. The value of I<sub>dc</sub> at the threshold value of V<sub>dc</sub>  $\approx$ V<sub>s</sub> (e.g., -500 V) increased with an increase in n<sub>e</sub>. When |V<sub>dc</sub>| exceeded the threshold, the spatial n<sub>e</sub> profile and the I<sub>dc</sub> dependence were changed relative to the electrical characteristics of the dc superposition; this led to a change in the location of the maximum n<sub>e</sub>, the width of the area of n<sub>e</sub> depletion in front of the electrodes, and a transition in the electron heating modes.

### 1. Introduction

Using halogen-containing gases for sputter etching increases the rate of etching; this was first described by Hosokawa et al.[1] This technique is also known as reactive-ion etching, [2] and it is widely used for capacitively coupled plasma (CCP); it is indispensable for fabricating ultralarge-scale integrated circuits (ULSIs).[3] In a conventional CCP, two parallel-plate-type electrodes are used, and radio-frequency (rf) power is applied, either independently or at the same time. Here, one of the electrodes (the "lower" electrode) is used to set up a wafer, and the counter electrode (the "upper" electrode) is powered for plasma generation.[4-17] The ion energy and flux at the electrodes can be controlled separately by discharges driven at different frequencies: the high-frequency voltage controls the charged particle density, and the low-frequency voltage controls the acceleration of the ions in the sheaths. This type of reactor is referred to as a dual-frequency CCP reactor.[7,8] Because a large wafer size and high excitation frequency are used, it is necessary to ensure that the plasma etching is uniform. Recently, in commercial reactors, a negative direct current (dc) voltage has been superposed to the upper electrode in parallel-plate-type CCPs [18]. The dc-superposed CCP etcher meets these requirements; it improves the uniformity and suppresses the profile distortions due to small features, which are the most likely cause of surface charging effects.[19,20] Therefore, the effects of dc superposition need to be further analyzed in a comprehensive way. In a previous paper, we demonstrated that the electron density increases as the voltage of the dc superposition increases [12]; this increase with the dc voltage has also been shown for the radical density [11].

Here, we consider the characteristics of a dual-frequency CCP, both with and without dc superposition. We measured the electron densities in a dual-frequency CCP with a superposed negative dc voltage and under various conditions. The electron density is considered to be the most important parameter when evaluating the characteristics of the plasma. In our experiments, we considered the controllable parameters to be the very-high-frequency (vhf) power, the rf power, and the negative dc bias. The observed characteristics will be discussed in terms of the dc self-bias, dc superposition, electron heating, and surface charge neutralization.

### 2. Background

Before discussing our results, we will briefly summarize the background of dc superposed CCPs, with respect to 1) the relationship of *self-bias voltages* on CCP discharges to geometrical and electrical symmetry, 2) *dc superposition* onto the CCP electrode, 3) *electron heating* and losses to the wall, and 4) *surface charging neutralization* and electron shading effects inside small features.

First, the self-bias voltage of CCP discharges is a key feature of the electrical behavior of a CCP discharge system. The sheath model was an early (1972) attempt to understand the self-bias voltage.[21] The development of the dc self-bias voltage can be modeled using the sheath capacitances of the self-consistent sheaths formed in front of each electrode. Simply put, the CCP discharge systems are modeled by representing the sheath as a capacitance. The dc self-bias,  $V_s$ , that is developed is expressed as  $V_s = (C_p - C_g)/(C_p + C_g)V_{rf}$ , where C is the capacitance, the subscripts p and g stand

for "powered" and "grounded" electrodes, respectively, and  $V_{rf}$  is the applied rf voltage.[22] Note that  $V_s$  is generated when there is an asymmetric discharge. When there is a single-frequency discharge and one electrode is powered and the other is grounded, this leads to high sheath voltages and high ion energies at the smaller electrode.[23]

In the 1990s, it was accepted that in dual-frequency CCPs, the discharges have separate functions: the high-frequency voltage sustains the plasma, and the low-frequency voltage controls the acceleration of the ions in the sheaths.[4-6] For the discharges of dual-frequency CCPs, the equivalent circuit is obtained by adding the blocking capacitor and another rf source to the lower electrode, as shown in Fig. 1(b). The modified circuit has two blocking capacitors,  $C_{lower}$  and  $C_{upper}$ . It is important to note that the capacitances are divided by the application of voltages on the sheath capacitances,  $C_{sh}$ . This voltage balance model can also be used to understand the electrical characteristics.

In the 2000s, Czarnetzki *et al.* introduced a more detailed voltage balance model to explain the electrical behavior of the multiple-frequency CCP discharges.[23] The CCP discharges are symmetric, i.e., the surface areas of the powered electrode,  $A_p$ , and the grounded electrode,  $A_g$ , are either identical  $(A_p = A_g)$  or asymmetric  $(A_p \neq A_g)$ .[23] A schematic of the equivalent circuit with potential  $\Phi$  is shown in Fig. 1(a). The voltages at each node are  $\overline{V}_{\sim}(t) = \overline{V}_C[\tilde{q}] + \overline{V}_{sp}[\tilde{q}] + \overline{V}_{sg}[\tilde{q}] + \overline{V}_b[\tilde{q}]$ , where  $\overline{V}_{\sim}$  is the applied voltage waveform,  $\overline{V}_C$  is the blocking capacitor,  $\overline{V}_{sp}$  and  $\overline{V}_{sg}$  are the voltage drops across the powered and grounded electrode, respectively,  $\overline{V}_b$  is the bulk plasma voltage, and  $\tilde{q} = q(t) = Q(t)/Q_0$  is the normalized space charge in the sheath as a function of time.[23] The positive charges are in the sheath at each of the electrodes, and the normalized total charge is located inside the sheaths; thus, we have  $q_t(t) = q(t) + q_{sg}(t)$ , where  $q_{sg}$  is the normalized positive space charge in the sheath at the ground electrode.[23] It is worth noting that positively and negatively charged particles are lost independently at the walls, and the electronegative electron flux  $\Gamma_e$  must be balanced by the electropositive ion flux  $\Gamma_i$  on the floating electrode. Thus, within a single frequency period,  $q_t$  is determined by the continuous loss of positive ions to the electrode, due to the Bohm fluxes [24,25], and by the instantaneous loss of electrons to the electrode at the phases of sheath collapse. A constant current develops throughout the plasma, and an additional electric field is necessary to equalize the current. The drops in voltage across the sheath are associated with these capacitances, and they can be represented by an integral of charges, based on Gauss' law.[26]

Recently, it has been proposed that the electrical asymmetry effect (EAE) can generate a variable  $V_s$  by controlling the phase shifts between multiple frequencies.[23] In details, within the rf period, sheath voltages instantaneously oscillated. The maximum voltages were linked the via symmetry parameter  $\varepsilon = \left| \frac{\max V_{sg}}{\max V_{sp}} \right| \approx \left( \frac{A_p}{A_g} \right)^2 \frac{\bar{n}_{sp}}{\bar{n}_{sg}} \left( \frac{Q_{mg}}{Q_{mp}} \right)^2$ , where  $\bar{n}_s$  is the spatially averaged ion density in the sheath, and  $Q_m$  is the maximum charge in the given (g or p) sheath.[23] Thus,  $V_s$  can be written as  $V_s = \frac{\phi_{max} + \varepsilon \phi_{min}}{1 + \varepsilon}$ , where  $\Phi$  is the potential at the sheath.[27] Simply put,  $V_s$  is due to the unbalanced charges of the two electrodes.[28] We note that electropositive ions respond to the time-averaged electric field, and electrons follow the instantaneous electric field.[29] The model for the time-averaged integral of positive charges in the sheaths, s(t), is based on the instantaneous position of the boundary between the sheath and the plasma;  $s_{max}$  is the maximum extent of the sheath.[25] Ideally, the temporally averaged mean sheath voltage  $V_s$  is equal to the difference between the absolute values of the period-averaged sheath voltages,  $V_s = |\langle \phi_s^{(lower)} \rangle| - |\langle \phi_s^{(upper)} \rangle|$ . A schematic of the simplified plasma density (n<sub>e</sub>) and ion density (n<sub>i</sub>) profiles is presented in Fig. 1(c). The maximum sheath extent (as either thickness or width) is usually on the order of millimeters or less and on the order of tens of pascals or less.

Next, we note that it has been argued that the drop in voltage of the sheath,  $V_s$ , is controlled by the application of the dc voltage to the CCP electrode. The setup in a previous study considered two configurations of the applied dc and rf power.[8] In the first, the combination configuration, the electrodes are operated by separate rf and dc power supplies; this configuration can be further classified by having a grounded or floating rf electrode. In the second configuration, the dc is superposed onto the rf electrode. The configurations used in selected references are presented in Table I. In 1985, Kohler *et al.* first described the dc superposition and reported that the potential was slightly modified by the superposition of a dc voltage [22]. The combination of rf and dc discharge sources was first reported by Yamamoto and Okuda in 1956,[30] and later by Hassan and Fujita.[31] Thereafter, in the late 2000s, several researchers reported computational results that showed that when a negative dc voltage was applied to the electrode, the sheath structure was superposed electromagnetically by a combination of a dc sheath and an rf sheath.[32-35]

The computational studies were limited in the high-pressure regimes and dealt with the collisional discharges. For low-power discharges at low pressures, the electron heating mechanism in collisionless cases is dominated by stochastic (or hard-wall) heating from electrons interacting with the moving high-voltage rf sheath.[36] A high-voltage sheath is formed and driven by the combination of the dc and rf source,  $V = V_{rf} + V_{dc}$ . The time-varying electron density within the sheath was calculated by using the ion current density; a large dc bias was applied to the sheath, and Child's law was then used to calculate the electrical circuit across the sheath .[32,33] In addition, the bombardment of ions accelerated by the high-voltage sheath will enhance the emission of secondary electrons, and the electrons generated on the electrode surface will accelerate back into the plasma, thereby increasing the plasma density.[32-35] The electron heating mechanisms in the discharges sustained by the application of large rf voltages at low pressure have received much attention, in particular, in the case of dc superposition.

Third, the plasma is sustained by the balance between the electron heating and the losses to the wall. The particle and energy balance is expressed by the bulk ionization and total surface loss.[36] In the bulk plasma, the momentum transfer due to collisions between oscillating electrons and neutral particles absorbs the time-averaged power; this is known as ohmic heating. The discharges sustain this bulk heating mode, and so the current throughout the plasma is relatively low; this is known as the low-current  $\alpha$  mode.[37] In the low-pressure regime, the electrons reflected by the large decelerating field due to the moving high-voltage sheath obtain power from the oscillating sheath; this is known as stochastic heating.[36] The transition between modes, ohmic heating due to collisions, and collisionless stochastic heating have been reported previously.[38]

For discharges at low power and at low pressures, collisionless heating will primarily be absorbed by the external power supply. Godyak and Lieberman discovered that the dominant mechanism for collisionless heating was the interaction of stochastic (or hard-wall) heating with electrons [39,40]. At low pressure, collisionless stochastic heating is dominant, and the heating due to collisions is not significant.

In contrast to stochastic heating, in collisionless heating, the ions strike the electrode. Secondary electrons are generated and are accelerated back into the plasma. The current throughout the plasma is thus relatively high; this is known as the high-current  $\gamma$  mode.[37]

In general, secondary electrons are not produced by ionization at low pressures; however, Godyak *et al.* points out that at around 300 Pa and with high ion energy, the plasma is sustained by the secondary electrons alone; this is known as the  $\alpha$ - to  $\gamma$ -mode transition.[38-40] In 1958, Levitskii was the first to establish the  $\alpha$ - to  $\gamma$ -mode transition.[37,41] Subsequently, the  $\alpha$ - to  $\gamma$ -mode transition was reported by Lisovskiy *et al.*[42] and Donko *et al.*[43]

When there are multiple excitation frequencies, it has been proposed that there is a mode transition from ohmic to stochastic heating. Zhang *et al.* presented results for the transition from heating dominated by a dual-frequency stochastic mode to one dominated by direct current, and they argued that this induced the emission of secondary electrons [44,45]. For simplicity, they assumed that the transition was to the nontraditional  $\gamma$ -mode and the dc superposition was limited. At present, the consensus is that with the dc superposition, these behaviors are similar to the dc discharges; this type of heating is known as the  $\gamma$  mode. Under these conditions, the n<sub>e</sub> profiles can be considered in terms of the sheath voltage, which is modulated by the dc superposition.

Fourth, in particular, with plasma etching technology, the electron shading effects inside small features are determined by the precise control of the etched features.[46] In 1990, Hashimoto reported that the positively charged ions strike the inside of small features due to the acceleration of the time-averaged negative electric field that builds up toward the surface; however, the negatively charged electrons are prevented from reaching the bottom of the feature, and this results in thermal motions without an accelerating field. [47,48] At the bottom surface of a small feature, localized charging is due to the arrival and deflection of electrons within the feature, and thus the shape profiles are distorted; this results in commonly seen artifacts, such as trenching, bowing, notching, and twisting.[46] Thus, some researchers believe that the accelerated, ballistic transport of electrons may neutralize the charge that is built up on the bottom surface of a feature.[19-21] Wang and Kushner reported computational results obtained from the hybrid plasma equipment model (HPEM).[43,44] They calculated the behavior of secondary electron emissions from Ar when the lower electrode received power at either 10 MHz or 2 MHz, and the upper electrode received dc augmentation (also called dc superposition). The computational results showed that highly energetic secondary electrons impinged onto the lower electrode surface, and that this increased the random occurrence of holes or trench shapes that deviated from the vertical [49,50]. The secondary electrons that are emitted by the upper electrode irradiate the lower electrode, resulting in neutralization. This is believed to neutralize the charged surface; that is, the secondary electrons accelerate with the negative dc potential and neutralize the inside of the microstructure.

The ballistic transport of secondary electrons through a bulk plasma was reported by Khrabrov *et al.* The electron velocity distribution function (EVDF) of the lower electrode has not yet been clarified for this setup.[51] The secondary electrons originating from the dc-biased surface were accelerated to an energy nearly equal to the bias potential, and another portion of the electrons emitted from the rf electrode eventually bounced between the sheaths.[51] Xu *et al.* reported that the ballistic electrons originated as secondary electrons produced by ion and electron bombardment of the electrodes. The energy distribution of the ballistic electrons peaked at the value of the negative bias applied to the dc electrode.[52] Diomeda *et al.* reported that the EVDF striking the substrate extended all the way to  $V_{rf} + |V_{dc}|$ , the sum of the peak rf voltage and the absolute value of the applied dc bias.[53] In either the combination or superposition setup, the secondary electron emissions are determined by the EVDF features involving ballistic transport of electrons along the developed large sheath potentials. As a consequence, the secondary electrons accelerate back into the plasma, and ballistic transport carries them through the bulk plasma, where they impinge inside the small feature; this results in a possible neutralization of the effect of the local surface charge.

We have considered the mechanism that determines the interelectrode electron density profiles, and we summarized the background of the dc superposed CCPs with respect to 1) self-biased voltages, 2) dc superposition and dc combination, 3) electron heating and losses, and 4) surface charge neutralization. We evaluated the electron densities by balancing the surface losses of electrons with the ionizations, since the realistic discharges are too complex to understand; we do not have enough information to elucidate the mechanism that determines the electron density or the spatial density distributions.

#### 3. Experiment Setup

A surface wave (SW) probe (also known as a plasma absorption probe, PAP)

was used in this study [12,54] to measure the actual electron density (n<sub>e</sub>) in conventional fluorocarbon plasmas [54-61]. The SW probe measures the absorption of a surface wave exiting from the head of a dielectric-covered probe [54]. We emphasize that the SW probe is completely covered by dielectrics that surround the conducting antenna, and thus it differs in this way from other resonance probes, such as cut-off probes.[62] The surface waves propagate in the microwave frequency range, and the signal can be detected by a network analyzer.

A Langmuir electrostatic probe is commonly used to measure  $n_e$ .[36] Unfortunately, this probe cannot be used with fluorocarbon plasmas or with industrial fluorocarbon CCP etchers; in those situations, an insulating film is deposited onto the probe surface, and this interrupts measurement of the electrical current. Although microwave interferometry can be used to measure  $n_e$ , in spite of the deposition of a film, only a line-averaged density can be measured. Thus, the SW probe is better for the purposes of this study.

Figure 2 shows a schematic of a commercially available dual-frequency (60 MHz/13.56 MHz) CCP apparatus equipped with an SW probe measurement system [61]. A quartz tube with an outer diameter of 5 mm was inserted into a port in the cylindrical chamber wall. The SW probe head consists of a 5-mm-long monopole antenna at the end of a coaxial cable with a diameter of 0.8 mm. Microwave power of ~1 mW was fed to the probe head from a network analyzer (Agilent E5071C, possible sweep range between 100 and 8000 MHz), and the frequency varied from 500 MHz to 1500 MHz. The power reflection coefficient  $\Gamma$  was obtained as a function of frequency. The incident power is sharply absorbed at a certain frequency  $f_{abs}$ , because a standing mode of the SW resonates at the probe head. The observed absorption frequency  $f_{abs}$  (MHz) is

related to the electron density  $n_e$  (cm<sup>-3</sup>) as  $n_e = \beta \times 10^4 (f_{abs})^2$ , where  $\beta$  is the proportionality constant, which was determined by the plasma oscillation method to be 3.78 for the probe used in our experiments [12].

In measurements of the interelectrode profile, the canted probe head was moved vertically by rotating it with an offset of 20 mm, as shown in Fig. 2(b), and thus the horizontal position changed simultaneously with the vertical position; however, the change in the horizontal position was less than 20 mm, due to the uniformity of  $n_e$ . An electrode diameter of 300 mm was sufficiently large to measure the interelectrode  $n_e$ profile. The probe had a relatively large diameter (5 mm) in order to obtain spatial profiles with reasonable resolution. The spatial positions were normalized by the interelectrode distance. The positions  $Z_{center}$  and  $Z_{upper}$  are the center of the plasma bulk and the front of the upper electrode, respectively. Using the spatially resolved measurements, we obtained the spatial profile at 2 mm increments of step height.

The dc current at the upper electrode was measured during discharge; the current density per unit area,  $I_{dc}$ , was estimated by dividing the measured current by the area of the upper electrode. We should note that we neglected the nonuniformity of the current; i.e., we only considered the total current flowing through the entire upper electrode.

A gas mixture was introduced into the chamber at the upper electrode. The aperture had a diameter of 30 cm, and it could be biased for a negative dc voltage. The gas consisted of  $C_4F_8/N_2/Ar$  at flow rates of 10/100/800 standard cc per minute (sccm). The gas composition was the same as that commonly used for reactive ion etching. The amount of electronegative  $C_4F_8$  could affect the plasma parameters,[63,64] but we are not interested in analyzing the electronegativity. The gas flow rates were controlled by a

mass-flow controller, and the total pressure was stabilized at 5.33 Pa (40 mTorr) by an automatic pressure controller. The measurements were performed under standard conditions: vhf  $P_{high}$  of 0, 500, 800, 1500, and 2400 W at 60 MHz were applied to the upper electrode; and rf  $P_{low}$  of 800, 1500, and 2000 W at 13.56 MHz were applied to the lower electrode, which was a distance of 3 cm from the upper plate. In addition, a dc voltage of 0 to -1200 V was applied to the upper electrode.

#### 4. Experimental Results

# 4-1. Spatial profile of plasma density in a single-frequency CCP

We first investigated the dependence of the vhf power at the upper electrode without the negative dc bias. We note that the rf power was not applied at the lower electrode, that is,  $P_{low} = 0$  W. This single-frequency CCP discharge is similar to one with a powered upper electrode and a counter lower electrode that is electrically floated.

Figure 3 shows the spatial profiles of the plasma density  $n_e$  for different values of  $P_{high}$  ranging from 800 to 2000 W. At the center,  $Z_{center}$ , the  $n_e$  increased with increasing  $P_{high}$ . The bulk  $n_e$  dependence on  $P_{high}$  was nearly linear. In the spatial profiles, we observed a wider depletion width  $W_s$  of  $n_e$  in front of the lower electrode and a narrower width at the upper electrode. In the spatial profile,  $n_e$  was depleted on both sides in front of both the upper and lower electrodes. The width of the depletion may be linked closely with the thickness of the sheath; however, the probe was not large enough to evaluate the spatial resolution. The absolute sheath thicknesses could not be analyzed. Hypothetically, the sheath thicknesses for the lower and upper electrodes were represented by the depletion widths coupled with the capacitances of the external circuit, because the electron density drop at the lower electrode was larger than that at the upper one. We can provide a qualitative interpretation of the depletion widths: a larger width reflects a smaller capacitance, and this leads to a large voltage drop at the lower electrode.

Figure 4(a) shows the spatial profile of  $n_e$  as a function of  $V_{dc}$  for the case of dc superposition onto a single-frequency CCP electrode. When  $P_{low}$  was not applied and  $V_{dc}$  was superposed on  $P_{high}$ , the bulk  $n_e$  was greatly reduced. The application of a large  $V_{dc}$  moved the location of the maximum  $n_e$  slightly in the direction of the lower electrode. In the following paragraph, we briefly discuss the reduction in  $n_e$ , which was a factor in the previously discussed computational results for a single-frequency CCP with rf and dc bias superposition at the upper electrode and a grounded lower electrode.[32,33]

The dependence of  $n_e$  at  $Z_{center}$  on  $V_{dc}$  is shown in Fig. 4(b). As the dc bias superposition was strengthened to more than  $-250 V_{dc}$ , the bulk  $n_e$  decreased. The  $n_e$  depletion widths on both sides of the electrode were balanced when the dc bias applied to the upper electrode was more than  $-500 V_{dc}$ . This implied that when a lower frequency was applied, the sheath voltage drop at the lower electrode might be approximately -500 V. There is a large and asymmetric electrical flow, and thus the electrical currents are unbalanced due to the imbalance between the electron and ion fluxes onto the electrode surfaces; the electrode surface acted as an electron sink when the applied  $V_{dc}$  was high.

## 4-2. Spatial profile of plasma density in a dual-frequency CCP

Figure 5 shows the variation in the  $n_e$  profiles as a function of the interelectrode position. In Fig. 5(a),  $P_{high}$  was 800 W and  $P_{low}$  was 1500 W. When a large  $|V_{dc}|$  (more than 250 V) was applied, the area of highest  $n_e$  moved from the side of the upper electrode to the side of the lower electrode. Notably, when a  $P_{low}$  of 1500 W was applied, the increase in  $V_{dc}$  led to an increase in the bulk  $n_e$ . Simultaneously, the bulk  $n_e$  profile increased. The depletion width  $W_s^{upper}$  in front of the upper electrode was widened slightly, while the  $W_s^{lower}$  was narrowed slightly. The rf and dc sheaths combined, and the electrons trapped between the electrodes caused the  $n_e$  maximum position to move and the bulk  $n_e$  to increase. We estimated that for the vhf/dc sheath,  $V_s^{upper}$  was almost -100 V.

As shown in Fig. 5(b), when a relatively high  $P_{high}$  of 2000 W was applied to the upper electrode, the bulk  $n_e$  was approximately  $2.0 \times 10^{11}$  cm<sup>-3</sup>, and at the lower electrode,  $n_e$  remained constant. When we applied  $P_{high} = 2000$  W and  $P_{low} = 1500$  W, an increase in  $n_e$  could be observed at the lower electrode; however, the difference was very small. When a  $|V_{dc}|$  smaller than 250 V ( $V_{dc} > -250$  V) was applied, the bulk  $n_e$ increased slightly. When  $|V_{dc}|$  was increased beyond 500 V ( $V_{dc} < -500$  V), the bulk  $n_e$ again decreased. When  $|V_{dc}|$  was greater than 800 V ( $V_{dc} < -800$  V), the bulk  $n_e$ increased with  $V_{dc}$ .

The spatial profile had two modes, depending on whether  $V_{dc}$  was large or small. When  $|V_{dc}|$  was large (-800 V and -1000 V), the depletion width in front of the upper electrode  $W_s^{upper}$  was wider, and  $n_e$  was concentrated at the central position. When  $P_{high}$  was 2000 W,  $V_s^{upper}$  for the vhf /dc sheath was almost -500 V and  $V_s^{lower}$  was almost -250 V. When  $V_s^{upper}$  was greater than  $V_s^{lower}$ , we postulate that the flux is balanced by the upper electrode, which acts as a wall and absorbs electrons.

Figure 6(a–c) shows the interelectrode  $n_e$  at  $Z_{center}$  as a function of  $V_{dc}$  for different values of  $P_{high}$  (from 500 W to 1500 W) and different values of  $P_{low}$  (800 W, 1500 W, and 2400 W). As  $P_{low}$  increased, the base level of  $n_e$  increased approximately in proportion to  $P_{low}$ : 1.1, 1.3,  $1.5 \times 10^{11}$  cm<sup>-3</sup> for 500, 1500, 2400 W, respectively.  $|V_{dc}|$ increased with  $P_{high}$ , and then decreased close to  $V_{dc} \approx -400V$ . The locations of these bumps are indicated by the dotted lines and are labeled Regions II and III (the regions will be further discussed below). The bumps occur in the same region of  $V_{dc}$ . As  $P_{low}$ increases, the bumps are hidden by the increase in the  $n_e$  base level. When  $|V_{dc}|$  is large, the  $n_e$  dependence increases linearly with  $V_{dc}$ , and the range of  $V_{dc}$  is labeled Region IV when  $|V_{dc}|$  is high and  $P_{low}$  has decreased.

For simplicity, we divide the observed  $V_{dc}$  into four regions, determined by the dependence of  $n_e$  on  $V_{dc}$ . When  $|V_{dc}|$  is smaller than -300 V,  $n_e$  is not dependent on  $V_{dc}$  (Region I). Then,  $n_e$  increases linearly with  $P_{high}$ . However, when  $|V_{dc}|$  is large ( $V_{dc} < -600$  V), the  $V_{dc}$  dependence of  $n_e$  is nearly linear, regardless of the size of  $P_{high}$  (Region IV). When  $|V_{dc}|$  is large, the bulk  $n_e$  increases proportionally with  $|V_{dc}|$ . In between Regions I and IV, the  $V_{dc}$  dependence displays bumps. There are two types of bump: upward when  $|V_{dc}|$  is smaller (Region II), and downward when  $|V_{dc}|$  is larger (Region III). Interestingly, as  $P_{high}$  decreases, the bumps in the  $V_{dc}$  dependence on  $V_{dc}$  displays the characteristic bump feature. The voltage at the bump is probably the same as that of the sheath voltage drop at the counter lower electrode, and this may lead to confinement of the bulk plasma. We can estimate that in Region II,  $V_s^{upper} < V_s^{lower}$ , and in Region III,  $V_s^{upper} > V_s^{lower}$ . For the cases shown in Fig. 5(a),  $P_{low}$  of 800 W led to  $V_s^{lower} \approx -300$  V, and  $V_s^{upper}$  ranged from -300 V to -500 V and depended on  $P_{high}$ .

To the best of our knowledge, we are the first to report the spatial profile of  $n_e$  when dc superposition and  $P_{high}$  are applied to the upper electrode and  $P_{low}$  is applied simultaneously to the lower electrode. The  $n_e$  maximum position in the bulk and the  $n_e$  depletion widths  $W_s$  in the front of electrodes were determined by various conditions of the external power. We have already shown that these spatial profiles can aid understanding of the effects of dc superposition. As a summary of  $n_e$  at  $Z_{center}$ , Fig. 6(d) shows the  $n_e$  surface in the  $P_{high}$  and  $V_{dc}$  space.

#### 4-3. Current density of the upper electrode

Next, dc currents that flow through the upper electrode are evaluated by considering their relation to the characteristic bump behavior, which depends on  $V_{dc}$ . Figure 7 shows the dependence of (a)  $I_{dc}$  and (b)  $n_e$  as a function of the dc superposition voltage for  $P_{high}$  values ranging from 0 to 2000 W and a constant  $P_{low}$  of 1500 W. The values of  $n_e$  at  $Z_{upper}$ , close to the upper electrode, are plotted.

The conditions were the same as those of Fig. 5(a), where  $P_{low}$  was 1500 W and  $P_{high}$  was 800 W. When there was no  $I_{dc}$  flow at the upper electrode,  $n_e$  remained constant, although  $|V_{dc}|$  was more than 100 V ( $V_{dc} < -100$  V). If a  $P_{high}$  of 2000 W was applied,  $n_e$  remained unchanged, and  $V_{dc}$  was below -350 V. If  $V_{dc}$  exceeded -350 V,  $n_e$  increased and then decreased. Similar bump behaviors were observed at  $Z_{center}$ , as discussed above.

For the dependences on  $P_{high}$  and  $V_{dc}$  when  $P_{low}$  was held constant at 1500 W, the behavior of  $n_e$  near the upper electrode is shown in Fig. 7(b); there are four regions to consider. In Region I, only a small  $I_{dc}$  enters the upper electrode, and  $|V_{dc}|$  is small. Here, the transition points moved linearly from -100 V (for  $P_{high} = 0 \text{ W}$ ) to -350 V (for  $P_{high} = 2000 \text{ W}$ ). In Region III, both  $n_e$  and  $I_{dc}$  changed simultaneously when a bump occurred, which implies that in front of the electrode, they are linked; here,  $I_{dc}$  causes a drop in  $n_e$ , and  $|V_{dc}|$  is large. This occurs when  $|V_{dc}|$  is larger than 500 V and  $P_{high} = 2000$  W. In between Regions I and III, we have Region II, where  $I_{dc}$  gradually increases with increasing  $V_{dc}$ . When a large  $|V_{dc}|$  is applied, it is likely that the thickness of the sheath increases due to the high-voltage Child sheath formation, as shown in Fig. 5(b). As a result, the  $n_e$  was further depleted in front of the upper electrode, and thus there was a large drop in  $n_e$  in Region III.

The electron and the positive ion fluxes are balanced on the electrical floating electrode; the electrical current is given by  $I_{dc} = I_i - I_e + I_{se}$ , where  $I_{se}$  is the secondary electron emission current,  $I_i$  and  $I_e$  are the electrical current (AeI) collected at an electrode of area A, and the subscripts i and e indicate ions and electrons, respectively. Note that  $I_{dc}$  is due to the incidence of positive ions that are accelerated by the applied negative potential and the secondary emission of electrons due to the impact of the ions. As  $|V_{dc}|$  increased, the  $n_e$  dependence became that of Region IV; however, there were no clear  $I_{dc}$  transition behaviors.

Table II summarizes the results for  $n_e$  and  $I_{dc}$ ; the relationship between  $n_e$  and  $I_{dc}$  is shown for each region. In particular, in Region II, we note that the increase in  $n_e$  is due to the superposition of  $V_{dc}$ ,  $I_{dc}$ , and the saturated  $I_{dc}$ , and it increases gradually. A further increase in  $V_{dc}$  results in a small increase in  $I_{dc}$ .

The following is a summary of the experimental results presented above: 1) There is a monotonic increase in the electron density  $n_e$  when  $P_{high}$  is increased.

2) The depletion width  $W_s$  at the lower electrode was wider than that at the upper

electrode. The location of the maximum moves in the direction of the lower electrode.

- 3) When no  $P_{low}$  is applied, the bulk  $n_e$  decreases when  $V_{dc}$  is applied.
- 4) The bulk  $n_e$  increases only when a large  $P_{low}$  is applied.
- 5) The depletion width at the upper electrode becomes wider when large  $P_{high}$  is applied.
- V<sub>dc</sub> first increases with n<sub>e</sub> (Region II), then decreases (Region III), and finally increases linearly (Region IV).
- 7)  $I_{dc}$  is detectable when  $V_{dc}$  is applied. In Regions II and III,  $I_{dc}$  gradually increases with  $|V_{dc}|$  and then becomes almost constant in Region IV.  $I_{dc}$  increases only slightly with  $I_{se}$ .

#### 5. Discussion

### 5-1. Single-frequency CCP with V<sub>dc</sub> superposition

The spatial profile in which  $W_s^{lower} \gg W_s^{upper}$  can be interpreted as showing an interdependence among the voltages, capacitances, and depletion widths (e.g.,  $V_s \sim 1/C_s \sim W_s$ ), and  $P_{high}$  distributes the capacitances in an equivalent circuit, as shown in Fig. 1(b). The vhf power  $P_{high}$  is applied only to the upper electrode. Here, in the stochastic heating mode, the sheath dynamics appear at the boundaries of both the upper and lower electrodes. As shown in Fig. 1(b), the voltages result in a division of the capacitance. The result is that the capacitance of the lower electrode might be smaller than that of the upper electrode. Since the lower electrode had an electrostatic chucking system,  $C_{lower}$  should be lower than  $C_{upper}$ , and thus the voltage drop between the lower sheath and the lower blocking capacitor was large.

We note that when a small  $|V_{dc}|$  was applied, the  $I_{dc}$  was negligible. This means the electron and ion fluxes onto the electrodes were balanced by the electrically floating walls. As  $|V_{dc}|$  increased, we observed a gradual increase in  $I_{dc}$ , and the voltages were within a range of 100 V. The dc superposition appears to have changed the potential of the sheath edge. The electron loss at the wall was the most important factor in determining  $n_e$ , and the electron generation increased with increasing heating due to the  $P_{high}$  input. The flow of  $I_{dc}$  in the upper electrode simultaneously increases  $\Gamma_e$  at the other electrodes. The electrically connected ground acts as an electron sink, and the electrically floated electrode is charged by positive ions; it is thus instantaneously neutralized.

We now review the previously published computational results. Kawamura *et al.* reported results for particle-in-cell (PIC) Monte Carlo collision (MCC) calculations for the superposition of dc and rf at symmetrical CCP discharges [32,33]. They focused on the secondary electron emissions at the dc superposed electrode and considered different values for the secondary electron emission coefficient,  $\gamma$ . When there was only an rf discharge, the pressure was 45 mTorr with Ar at 4 MHz, and it was assumed to be a collisional case; they observed that the rf portions of the sheath lengths decreased as the density at the sheath edge increased. In the dc/rf combination case, the density is significantly asymmetric, with the density at the sheath edge a factor of 1.6 larger than that near the dc/rf electrode. The source of this asymmetry is the excess ionization from the higher-energy secondary electrons, which deposit most of their energy near the sheath.[32,33] As the pressure decreased to 10 mTorr, the plasma became nearly symmetric, with approximately the same integrated density in all areas; the increase in the diffusion losses due to the larger sheath width at the dc electrode (smaller bulk

plasma width) were compensated by the increase in discharge efficiency of the secondary electrons. The secondary electrons do not significantly perturb the symmetry, because most of them are absorbed.[32,33] This is a nontraditional  $\gamma$  mode, and it differs from the traditional  $\gamma$  mode, which is sustained by secondary electrons that appear in the dc discharge.

For the experimental apparatus used in this study, we cannot rigorously analyze the symmetry of the spatial distribution, but we can note that the observed symmetry was consistent with the computational results. The abnormal  $\gamma$  mode for the rf/dc combination with high  $|V_{dc}|$  was confirmed experimentally; that is, our experimental results were qualitatively supported.

#### 5-2. Dual-frequency CCP with $V_{dc}$ superposition

The stochastic heating for the dual-frequency CCP should be taken into account when independently forming a more complicated sheath in front of each electrode. Chabert and Braithwaite noticed that due to the instantaneous overlap of two frequencies, the sheath position oscillates, and consequently, the maximum extent of the collisional sheath s<sub>m</sub> is determined.[29] Note that s<sub>m</sub> is inversely proportional to the driving frequency.[29] When vhf power (60 MHz) was applied to the upper electrode, the sheath thickness was thinner than it was when rf power (13 MHz) was applied to the lower electrode.

From our results, it is unclear whether the heating mode is collisional or collisionless. The operating frequencies have been considered by various researchers. Abdel-Fattah and Sugai reported a transition to electron heating mode from collisional

ohmic heating at low frequencies for plasma surface heating in the VHF range.[65] Sahu and Han reported both ohmic and stochastic modes in a dual-frequency (13 and 320 MHz) CCP in the relatively low-pressure regime (30 Pa).[66] Turner discussed collisional ohmic heating in the bulk and in the sheath.[67-71] The other mode is momentum transfer from the edge of a high-voltage sheath; this is known as collisionless stochastic heating. Kawamura *et al.* considered electron heating modes in CCP.[72,73] Lafleur *et al.* reported computational results that showed that in multiple-frequency discharges, the total electron heating comprises collisional (ohmic) and pressure heating, and they observed an additional collisionless heating mechanism associated with electron inertia.[74] At high pressure, bulk plasma density abruptly increases following the transition, and the average electron energy drops to that sustained by secondary electrons when high dc voltages continue to be applied.[75] At low pressure, there is stochastic heating while the dc voltage increases, although the overall heating power decreases; ohmic heating is also observed.[75] As a result, when no  $V_{dc}$  is applied, the experimental results indicated that stochastic heating is dominant.

Denpoh and Ventzek reported the results of a method for tracing test particles.[34,35] In a two-frequency CCP setup for  $CF_4$  discharges, the upper electrode received overlapped dc and vhf power (60 MHz), and the lower electrode received rf power (2 MHz); they showed that the sheath thickness increased in front of the upper electrode, and the bulk electron density increased.[34,35] If rf power was supplied to the lower electrode, the sheath potential in front of that electrode repelled electrons; this action was lost if the rf power was removed. Our experimental results supplied further evidence for this. Thus, we again emphasize the importance of the rf supply at the lower electrode; it serves as a counter electrode for the dc superposed electrode and increases

the bulk n<sub>e</sub> and the emission of secondary electrons at the upper electrode.

Zhang *et al.* considered a PIC calculation for  $CF_4$ ; the upper electrode received vhf power (60 MHz) and dc, and the lower electron received rf power (2 MHz).[44,45, 76-78] When no dc bias is applied, the discharge is primarily maintained by the dual-frequency coupling effect. When applying a superposed dc voltage, the plasma density first increases, then decreases, and finally increases again.[44] They explained that this results in the transition between the four main heating modes: dual-frequency coupling, dc and dual-frequency coupling, dc source-dominant heating, and secondary electron dominant heating.[44] When the dc superposed and dc self-bias voltages are comparable, the coupling effect of dc and rf dominates the heating mechanism, and causes an increase in the plasma density. Depending on the balance between the voltages, the electron trapping in the bulk region is weakened, and hence the plasma density decreases. Finally, when the dc voltage is sufficiently large, the high-energy secondary electrons originating at the dc electrode contribute most of the ions, and this gives rise to another increase in the plasma density.[44]

The ionization rate can be estimated from the optical emission intensity of the excited gas. Gans *et al.* reported phase-resolved optical emission spectroscopic (PROES) observations of spatiotemporal electron-induced excitation. [79-81] In the future, a similar experiment is needed to determine the heating mode transition when there is a low-pressure discharge; the dc superposed dual-frequency CCP may show the spatiotemporal variation of the excitation rate. Very recently, Liu *et al.* reported that strong modulations in the plasma density and light emissions known as "striations" were observed in electronegative capacitively coupled rf (CCRF) plasmas.[82] They suggest that these striations are generated by the resonance between the driving radio

frequency and the ion-ion eigenfrequency, and that this modulates the electric field, the ion densities, and the energy gain and loss processes of the electrons in the plasma.[82] They pointed out that this is very sensitive to the species of ion, such as ions in  $O_2$ , SF<sub>6</sub>, or C<sub>4</sub>F<sub>8</sub> gas[82]; therefore, the disturbance in the electronegativity can be ignored in this case, because of the abundant dilution of Ar. However, such a detailed experiment is a topic for future investigation.

#### 5-3. Currents in the upper electrode, $I_{dc}$

In the dc superposition that induced the secondary emission of electrons at the dc electrode surface, the  $I_{dc}$  was observed experimentally in order to determine the mode transition. However, we should note that the complete relationship between  $n_e$  and  $I_{dc}$  was not determined. When a large  $|V_{dc}|$  is applied,  $I_{dc}$  can be used to monitor of strength of  $n_e$ , since it is closely related to the ion fluxes of the substrate. If we assumed the ion specie was  $Ar^+$  and that they had an electron temperature  $T_e$  of 5 eV, then an  $I_{dc}$  of 1.11 mA cm<sup>-2</sup> predicted an  $n_e$  of  $0.4 \times 10^{11}$  cm<sup>-3</sup>. The measured  $n_e$  was  $1.6 \times 10^{11}$  cm<sup>-3</sup>, which was approximately consistent with  $I_{dc}$ , since it is only off by a factor of four. Thus, this approach can be used to obtain a rough estimate of  $n_e$ .

In Region II, when an intermediate dc bias was applied (i.e., between -300 and -600 V), the bulk n<sub>e</sub> was reduced. As the dc voltage further increased, the discharge gradually switched from the stochastic mode ( $\alpha$ ) to the mode dominated by secondary electrons ( $\gamma$ ). As the accelerating ions impinge onto the upper electrode surface, the secondary electrons are emitted from the surface. The secondary emissions are dependent on the striking energy of the ions, and the secondary emission coefficients

ranged from 0 to 0.1. The secondary electrons approaching the sheath are repelled when the sheath potential exceeds the upper electrode sheath potential. Region II then appears with a gradual rise in  $I_{dc}$ , which reflects the amplitude of the secondary emissions due to impinging ions accelerated by the sheath potential. Note that the self-biased voltages applied at the upper electrode (for vhf power) were approximately -200 V for  $P_{high} =$ 800 W, and the fluctuations were roughly 100 V.

## 5-4. Spatial profiles of $n_e$ for $V_{dc}$ superposition

In the above discussion, we summarized the effects of the dc superposition on the dual-frequency CCP in terms of the dc self-bias, the transition of the heating mode from  $\alpha$  to  $\gamma$ , and the secondary electron emissions from the dc-biased surface.

As  $|V_{dc}|$  increases,  $n_e$  first increases, then decreases, and finally increases again. This behavior was first reported by Yamaguchi *et al.* [8] and later by Zhang *et al.* [39,40]. When a high  $P_{low}$  (1500 W) was applied to the lower electrode and the  $V_{dc}$ increased, they observed the following: as  $V_{dc}$  increased further,  $n_e$  decreased.

We modeled the bulk plasma density and its spatial profile when the dc was superposed in the dual-frequency CCP. Figure 8 shows a schematic of the model. Without the application of  $P_{low}$ , electrons do not remain in the bulk plasma, and these results in them being lost to the lower electrode. This is consistent with Kawamura's computational results.[28,29]

For high secondary emissions, it was necessary that the accelerated ion energies exceed 500 V; the heating then moved to the  $\gamma$  mode if V<sub>dc</sub> was sufficiently larger than -500 V. Importantly, if the sheath potential of the counter electrode, opposite

the electrode to which  $V_{dc}$  was applied, was less than that at the upper electrode, then the electrons are not trapped in the bulk plasma region. This means that the applied  $V_{dc}$ increases the loss of electrons when they recombine at the wall. This is why we began by experimentally determining the spatial profile of  $n_e$  when  $V_{dc}$  is applied in a dual-frequency capacitive coupled discharged plasma.

We also wish to consider the emission of secondary electrons. When the high-energy electrons impact the surface of the lower electrode, they provide a dc bias superposition. The apparently large potential of the sheath in front of the upper electrode accelerates the electrons that are emitted. When  $P_{low}$  is small, it has low self-bias, and the secondary electrons pass through the sheath in front of the lower electrode. However, when the self-bias is high at the lower electrode, the secondary electrons are unable to overcome the barriers of the sheath potential. This implies there is no neutralization of the charge of hot electrons in the microstructures of the lower electrode.

### 6. Conclusions

In a dc superposed dual-frequency CCP apparatus, we used an SW probe to experimentally measure the interelectrode electron density profile under conditions encountered in practice and using the electronegative gas of  $C_4F_8$ . By changing the position of the probe, we were able to measure changes in the spatial profile when a negative dc bias was applied to the upper electrode. When rf power was applied to the lower electrode, the sheath potential there changed the spatial profile and increased the bulk electron density. When the rf power at the lower electrode was as large as the potential of the sheath at the upper electrode, the secondary electrons were trapped. We suspected that the spatial profile of  $n_e$  reflected the blocking capacitors and the transition of the heating mode from  $\alpha$  to  $\gamma$ . The heating mode was changed when the secondary electron emissions accelerated due to the high-voltage sheath; it is possible that part of this may be ballistic transport to the counter electrode, thereby neutralizing the local surface charge. This can be summarized by saying that the heating of the secondary electrons was dominated by transitions from the  $\alpha$  mode to the  $\gamma$  mode. At the voltage at which this transition occurs, the negative dc bias causes a characteristic bump in the bulk  $n_e$ . The application of the negative dc bias maintains or slightly decreases the  $n_e$ , but as the voltage reaches that of the counter sheath potentials, which fluctuate by hundreds of volts,  $n_e$  increases. Thus, the dc superposition affects the spatial profile of  $n_e$  only when there is a large rf power on the lower electrode.

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Table I. Previous studies of dc/rf combination and dc superposition onto the rf electrode.

Exp: e	experiment	ntally;	and	Comp:	computatio	onally.
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Туре	Short description				
1. rf/dc combination					
	Exp: dc and rf fields perpendicularly superposed	[30]			
	Air discharge was reported.				
	<b>Exp</b> : dc $CF_4$ or Ar discharge at 1mTorr with				
rf/dc combination with electrically grounde dc source rf supply	<sup>a</sup> modulation of rf (1k-28 MHz) was reported.				
	<b>Comp</b> : combined dc/rf (4-13 MHz) discharge at	[32]			
÷ • ÷	60 mTorr (4.5 Pa) was reported. Secondary	[33]			
rf/dc combination with electrically floating dc source rf supply	electron emissions				
	Exp: dc/rf (13.56 MHz) hybrid Ar discharge was	[72]			
= '-' =	reported. Highly energetic electron was measured	[73]			
	<b>Comp</b> : dc/rf combined discharge or hybrid dc/rf	[76-			
	CF <sub>4</sub> discharge was reported.	78]			
2. dc superposition onto rf electrode					
	Exp: dc-biased rf (13.56MHz) Ar discharge with	[22]			
	the counter grounded were reported. Ion energy				
	measurements were conducted.				
	<b>Comp</b> . hybrid dc/rf (60MHz) discharge with the	[34]			
	counter rf bias (2MHz) was reported. Highly	[35]			
	energetic electron was calculated.				
	<b>Exp</b> : dc superposed vhf (60MHz) C <sub>4</sub> F <sub>8</sub> /N <sub>2</sub> /Ar	[11]			
another rf supply	discharge at 5.3 Pa with the counter rf bias (13.56	[12]			
(floated) or	MHz) was reported.				
grounded $dc$ source $-$	<b>Comp</b> : hybrid dc/dual rf (60MHz) CF <sub>4</sub> discharge	[44]			
	at 70mTorr (5.3 Pa) with the counter rf biased	[45]			
	(2MHz) was reported.				
	Comp: dc-augmented rf (10-100 MHz)	[49]			
	$Ar/C_4F_8/O_2$ discharge with the counter rf bias	[50]			
	(1-2MHz) was reported. Ion energy/angle				
	distribution and secondary electron emission were				
	calculated.				

Regions	Ι	II	III	IV		
	$V_{dc} < V_s^{upper}$	$V_{dc} \simeq V_s$	$V_{dc} > V_s$	$V_{dc} >> V_s$		
V <sub>s</sub> <sup>upper</sup>	upper <	lower	upper > lower			
$\mathbf{V}_{s}^{lower}$						
bulk n <sub>e</sub>	const.	1	7	$\uparrow \uparrow$		
I <sub>dc</sub>	0	7	$\rightarrow$	7		
$W_{s}^{(upper)}$	upper		upper			
(lower)	$\wedge$	۱.	V			
W <sub>s</sub> <sup>(lower)</sup>	lower		lower			
Z <sup>max</sup>	slightly upper		enter	move to lower		

Table II. Plasma characteristics in each  $V_{dc}$  region. V: voltages; W: depletion widths; see text for other abbreviations.

## Figure captions

- Figure 1 (a,b) Equivalent circuit models of a capacitively coupled plasma (CCP)
  discharge, (c) time-averaged charged particle density distribution between the electrodes, (d) time-averaged potential between the electrodes. Φ: potential; V: voltage; Γ: flux.
- Figure 2 (a) Schematic of the experimental setup of the dual-frequency capacitively coupled plasma etching reactor, and (b) using the SW probe to measure the interelectrode spatial distribution of electron density.
- Figure 3 Spatial profile of the n<sub>e</sub> dependence on the upper vhf power (from 500 W to 2000 W), with and without application of the lower rf power and the upper negative dc bias.
- Figure 4 (a) Dependence of the electron density on the voltage of the dc bias, and (b) spatial profile of the electron density as a function of the interelectrode position when no rf power is applied to the lower electrode.
- Figure 5 Spatial profile of electron density as a function of the interelectrode position for different levels of vhf power: (a) 800 W, and (b) 2000 W.
- Figure 6 Interelectrode electron density as a function of the dc superposition voltage for different levels of vhf power (from 800 W to 1600 W) and different levels of rf power applied to the lower electrode: (a) 800 W, (b) 1500 W, and (c) 2400 W. (d) Electron density surface showing the dependence on the dc bias and the vhf power.
- Figure 7 Dependence on the applied negative dc bias voltage when the rf power was 1500 W for (a) the current density flowing through the upper electrode, and (b) the electron density. Data are shown for vhf power ranging from 800 W to 2000

W.

Figure 8 Proposed model for determining the spatial profile of  $n_e$  in a dual-frequency CCP with the superposition of a negative dc bias. (a, c) small  $P_{low}$ , and (b, d) high  $P_{low}$ ; (a, b) without  $V_{dc}$ , and (c, d) with  $V_{dc}$ .



Figure 1



b) Upper (VHF/dc)



Figure 2



Figure 3



Figure 4



Figure 5



Figure 6



Figure 6 (contd.)



Figure 7



Figure 8