

MECHANICAL CHARACTERIZATION OF SiC FILM AT HIGH TEMPERATURES BY TENSILE TEST

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ABSTRACT

This paper reports the mechanical properties of silicon carbide (SiC) films at elevated temperatures up to 500°C. Poly-crystalline SiC film (poly-SiC) was deposited by LPCVD on a silicon wafer and patterned into a free-standing specimen on an “on-chip” tensile test device. The fracture strength of poly-SiC films showed little temperature dependence over the test temperature range. The tensile strength was 2.89 GPa at room temperature (RT), and decreased slightly to 2.66 GPa at 500°C. The fracture surface at 500°C showed almost the same morphology as that at RT, without any slippage. The potential of SiC films as a material for micromechanical devices working at high temperatures has been heretofore confirmed.

1. INTRODUCTION

The demands for sensors that can be operated at high temperature are increasing in fields such as the automobile, aerospace, energy, and chemical industries. Si-based MEMS (microelectromechanical systems) physical sensors have already been commercialized for detecting physical quantities such as pressure, acceleration, angular, and velocity. Single-crystal-silicon (SCS) is an excellent mechanical material in the room temperature range. However, the strength of SCS films with a thickness less than 5 μm significantly decreases above 300°C [1], and micromechanical structures show plastic deformation above 500°C.

Silicon carbide (SiC) is a promising material for application in harsh environments such as being exposed to high temperature, high radiation, corrosive chemical, etc. SiC has a high elastic modulus, high mechanical strength, high thermal resistance, and excellent resistance to corrosion. Various approaches to fabricating SiC thin films on silicon wafers have been developed in the last twenty years. Of particular interest here is that researchers have demonstrated SiC MEMS sensors for harsh environment applications [2-4]. To understand the reliability of these devices, it is necessary to study the mechanical properties of SiC films in the same dimensions and fabrication processes of MEMS devices in order to identify potential size effects and process dependencies. While several researchers have reported the mechanical properties of micron-sized SiC films [5-9] for MEMS devices, very little has been done at high temperatures [10]. The mechanical properties at high temperatures are necessary for poly-SiC based MEMS sensors for harsh environments.

We have previously developed a tensile testing system

for mechanical characterization of thin films at high temperatures [1]. In the present study, we fabricated SiC tensile specimens based on our tensile testing systems. We report the tensile testing results, up to 500°C, of polycrystalline SiC (poly-SiC) films deposited by LPCVD, and show the films' mechanical properties at high temperatures for MEMS structures.

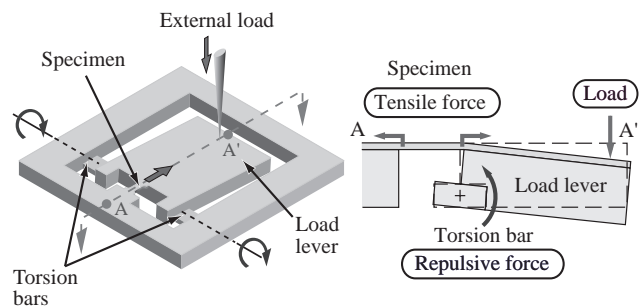
2. DEVICE AND FABRICATION

“On-chip” tensile test

We applied the “on-chip” tensile test system developed by Sato [11] to evaluate mechanical properties of poly-SiC thin films at high temperatures. In this method, we use a unique test device that consists of a thin film tensile specimen and a loading system, as shown in Fig. 1. The loading system is composed of torsion bars, a load lever, and a support frame. When a normal load is applied to the load lever, the lever rotates around the axis of the torsion bars, and the specimen is uniaxially stretched (Fig. 1 (b)). The applied load balances the tensile force to the specimen and the repulsive force of the torsion bars before the specimen fractures. The tensile force is calculated from the applied load by subtracting the repulsive force of the torsion bars, which can be measured after the specimen fractures.

This testing method has two advantages for testing brittle materials like SiC. First, this device enables easy tensile testing of thin film materials only by pushing-down action of the load lever end without having to deal with bothersome specimen gripping problems. Second, the system is suitable to detect small strains of SiC which is a very stiff material. The elongation of the specimen is proportionally enlarged and detected at the loading point of the lever end according to the lever ratio of the system.

The test device is clamped on the heating stage containing cartridge heaters and a thermocouple, and the



(a) Schematic of the test device (b) Cross-sectional view
Figure 1. “On-chip” tensile testing method.

temperature of the stage can be controlled during the test. The details of the measurement system are described in [1].

Fabrication process of test chip

Fig. 2 shows the fabrication process of the SiC film specimen on the tensile test device. We prepared (100) single crystal silicon with a thickness of 450 μm . A 2- μm -thick LTO layer was deposited on the front side to protect the silicon surface. Poly-SiC was then deposited on the LTO

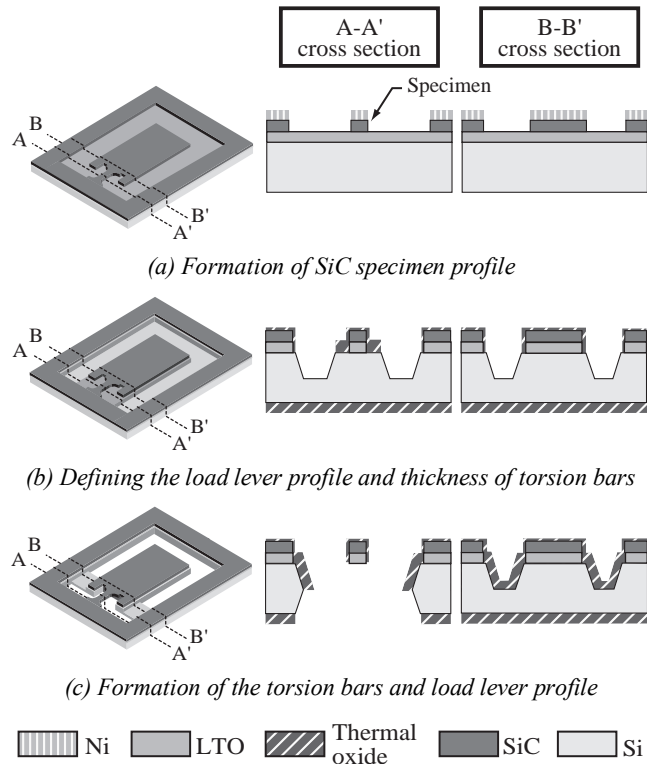


Figure 2. Fabrication process of SiC film test device.

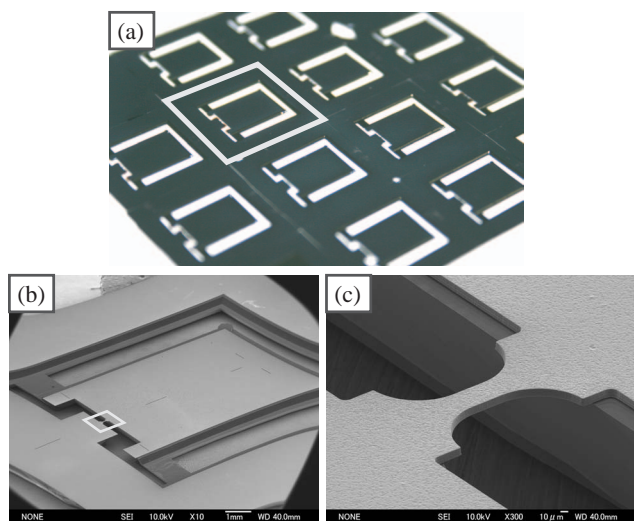


Figure 3. Fabricated testing device. (a) Arrayed tensile devices on wafer, (b) SEM image of the device, (c) SiC film specimen.

layer by LPCVD deposition; the total thickness was achieved by accumulative deposition in three runs. A Ni mask was deposited and patterned, and used as the etch mask to define the poly-SiC specimen profile by inductively-coupled-plasma etching using SF_6 (Fig. 2 (a)). After removing the Ni RIE mask, a thermal silicon dioxide layer was grown and patterned on the top surface of the Si substrate to serve as an etching mask for the next step, which was anisotropic wet etching using a TMAH solution. This bulk etch of the silicon substrate was continued until the desired thickness of the torsion bars was attained (Fig. 2 (b)). Silicon dioxide was thermally grown and patterned again on the back side of the Si substrate, and wet bulk etching was again used to define the profiles of the torsion bars and load lever (Fig. 2 (c)). Finally, the oxide mask was removed using buffered HF solution.

The wafer was cut into 15-mm squares. A typical fabricated poly-SiC test specimen is shown in Fig. 3. The specimen size is 50 or 100 μm long, 45 μm wide, and has an average thickness of 11.7 μm .

3. RESULTS AND DISCUSSION

Tensile testing of the poly-SiC specimens was carried out at temperatures ranging from room temperature (RT) to 500°C. Four tests were conducted at RT, three at 300°C, and four at 500°C. Figure 4 shows the stress-strain relationships at RT and 500°C. The measured data represented by the open circles show that the poly-SiC specimens deformed

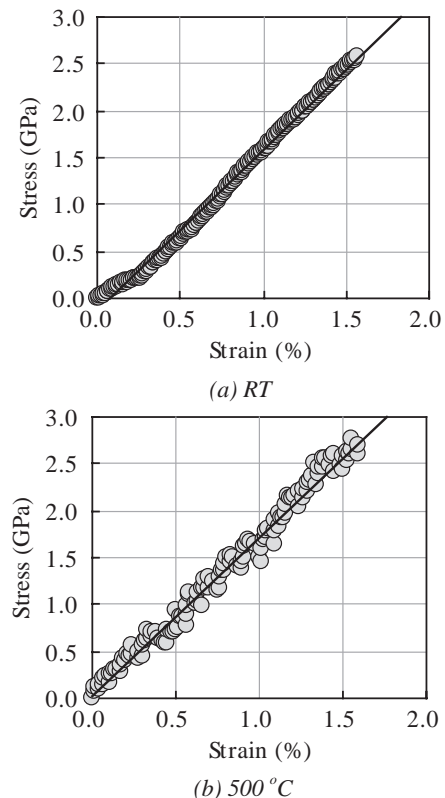
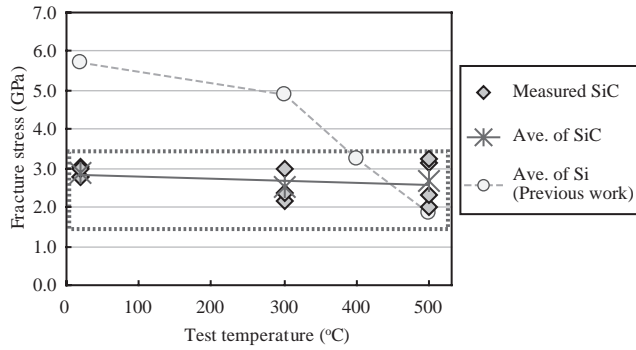


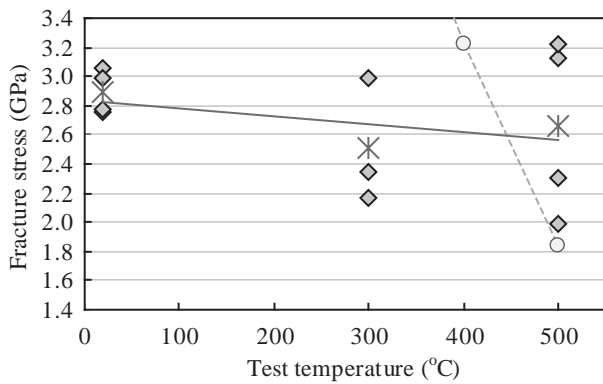
Figure 4. Stress-strain relationship.

elastically until they fractured, although the graph at 500°C includes some thermally induced noise. All specimens showed the expected brittle fracture behavior like above.

Figure 5 shows the relationship between fracture stress of poly-SiC films and test temperature, as well as a comparison with previous study of SCS films [1]. The average values at each temperature are summarized in Table 1. The fracture stress of poly-SiC film at RT was 2.89 GPa, which is relatively large compared to other reports [5, 6].



(a) Full scale graph from RT to 500°C



(b) Magnified graph of the dashed line

Figure 5. Measured fracture stress of SiC film at each temperature.

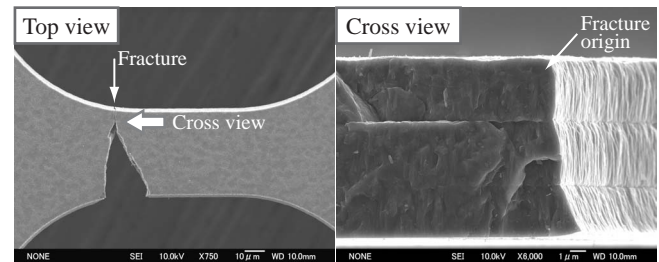
Table 1. Average value of fracture stress of SiC and SCS.

| Test Temp. (°C) | | RT | 300 | 400 | 500 |
|--------------------------------|------------------------|-------|-------|------|-------|
| SiC | Average Thickness (μm) | 11.73 | 11.75 | – | 11.69 |
| | Number of Tests | 4 | 3 | – | 4 |
| | Fracture Stress (GPa) | 2.89 | 2.50 | – | 2.66 |
| SCS <110> loading [1] | Average Thickness (μm) | 3.97 | 4.33 | 4.35 | 4.26 |
| | Number of Tests | 5 | 5 | 5 | 5 |
| | Fracture Stress (GPa) | 5.73 | 4.91 | 3.22 | 1.84 |

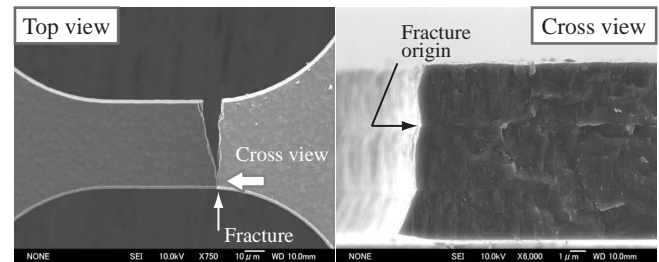
This difference can be a result of differences in the fabrication process conditions from film deposition to specimen patterning. In particular, etching conditions that dominate the morphology of the specimen sidewall, where the weak point of thin film material is prone to occur. As the test temperature increased, fracture stress slightly decreased, reaching 2.66 GPa at 500°C with a slight increase of variation.

In our previous work on SCS film [1], the fracture stress was 5.73 GPa at RT, more than twice that of the SiC film. The possible reasons of such a low strength of SiC relative to SCS can be that: (i) the poly-SiC film here is fabricated by from three separate depositions, providing an opportunity to introduce flaws in the specimen; (ii) the RIE etched sidewall morphology of the poly-SiC specimen is different than the previous SCS specimen; and (iii) the SiC specimen thickness is much larger than that of SCS. Actually, a size effect on the strength with Si has been reported elsewhere [12]. With SCS films, the fracture stress decreased significantly above 300°C, and further decreased to 1.84 GPa at 500°C with cross-slippage in the crystal. Here, we find that the fracture strength of poly-SiC films is stable in a range from RT to 500°C.

Figure 6 shows SEM images of poly-SiC specimens fractured at RT and 500°C. All specimens fractured at the end of the straight portion, where the stress concentration occurs. The cracks originated at the corner of the rectangular cross section or at a flaw between the layers of three consecutive LPCVD depositions. The multi-layer film has higher probability of fracture because of flaws at the layer interfaces. We speculate that a film with the same total thickness from one continuous deposition will have improved tensile strength. The fracture surfaces at 500°C showed almost the same morphology as those at RT,



(a) RT



(b) 500°C

Figure 6. SEM images of fractured SiC specimens at RT and 500°C.

without any slippage in the crystals or grain boundary. This indicates that the fracture properties do not change at 500°C. These results demonstrate the high mechanical robustness of SiC films at high temperatures and guarantee reliable MEMS devices operating under such harsh environments.

4. CONCLUSION

“On-chip” tensile testing was used for mechanical characterization of poly-SiC thin films at high temperatures. Poly-SiC films deposited by LPCVD at a thickness of 11.7 μm were patterned into tensile specimens based on an “on-chip” tensile test device. Tensile testing of poly-SiC films was carried out in a range from RT to 500°C. The fracture stress of poly-SiC film at RT was found to be 2.89 GPa. The value then slightly decreased as the test temperature increased, reaching to 2.66 GPa at 500°C. Compared to previous results from SCS films that showed a significant decrease in strength at temperatures above 300°C, SiC films exhibited highly stable mechanical strength. Fracture surfaces of poly-SiC films at 500 °C showed almost the same morphology as those at RT, without any slippage. We found that SiC films are excellent for high-temperature MEMS devices.

ACKNOWLEDGEMENTS

This work was partially supported by research fellowships of the Japan Society for Promotion of Science for Young Scientists, and of the 21st COE program (Micro- and Nano-Mechatronics for Information-based Society) from the Ministry of Education, Culture, Sports, Science, and Technology, Japan.

REFERENCES

- [1] S. Nakao, T. Ando, M. Shikida, K. Sato, “Mechanical properties of a micron-sized SCS film in a high-temperature environment”, *J. Micromech. Microeng.*, vol. 16, no. 4, pp. 715-720, 2006.
- [2] L. Chen, M. Mehregany, “A silicon carbide capacitive pressure sensor for high temperature and harsh environment applications”, in *Digest Tech. Papers Transducers’07 Conference*, Lyon, June 10-14, 2007, pp. 2597-2600.
- [3] R. G. Azevedo, D. G. Jones, A. V. Jog, B. Jamshidi, D. R. Myers, L. Chen, X. Fu, M. Mehregany, M. B. J. Wijesundara, A. P. Pisano, “A SiC MEMS resonant strain sensor for harsh environment applications”, *IEEE Sensors J.*, vol. 7, no. 4, pp. 568-576, 2007.
- [4] D. J. Young, J. Du, C. A. Zorman, W. H. Ko, “High-temperature single-crystal 3C-SiC capacitive pressure sensor”, *IEEE Sensors J.*, vol. 4, no. 4, pp. 464-470, 2004.
- [5] W. N. Sharpe, Jr., O. Jadaan, G. M. Beheim, G. D. Quinn, N. N. Nemeth, “Fracture strength of silicon carbide microspecimens”, *J. Microelectromech. Syst.*, vol. 14, no. 5, pp. 903-913, 2005.
- [6] K. M. Jackson, “Fracture strength, elastic modulus and Poisson’s ratio of polycrystalline 3C thin-film silicon carbide found by microsample tensile testing”, *Sensors and Actuators A*, vol. 125, pp. 34-40, 2005.
- [7] J. J. Bellante, H. Kahn, R. Ballarini, C. A. Zorman, M. Mehregany, A. H. Heuer, “Fracture toughness of polycrystalline silicon carbide thin films”, *Appl. Phys. Lett.*, vol. 86, 071920, 2005.
- [8] V. Hatty, H. Kahn, J. Trevino, C. A. Zorman, M. Mehregany, R. Ballarini, A. H. Heuer, “Fracture toughness of low-pressure chemical-vapor-deposited polycrystalline silicon carbide films”, *J. Appl. Phys.*, vol. 99, 013517, 2006.
- [9] H. D. Espinosa, B. Peng, N. Moldovan, T. A. Friedmann, X. Xiao, D. C. Mancini, O. Aiciello, J. Carlisle, C. A. Zorman, M. Mehregany, “Elasticity, strength, and toughness of single crystal silicon carbide, ultrananocrystalline diamond, and hydrogen-free tetrahedral amorphous”, *Appl. Phys. Lett.*, vol. 89, 073111, 2006.
- [10] W. N. Sharpe, Jr., M. Zupan, K. J. Hemker, “Tensile testing of MEMS materials at high temperatures”, *Proc. 12th International Conference on Experimental Mechanics*, Bari, Italy, Aug. 29-Sep. 2, 2004.
- [11] K. Sato, T. Yoshioka, T. Ando, M. Shikida, T. Kawabata, “Tensile testing of silicon film having different crystallographic orientations carried out on a silicon chip”, *Sensors and Actuators A*, vol. 70, pp. 148-152, 1998.
- [12] T. Tsuchiya, O. Tabata, J. Sakata, Y. Taga, “Specimen Size Effect on Tensile Strength of Surface Micromachined Polycrystalline Silicon Thin Films”, *J. Microelectromech. Syst.*, vol. 7, pp. 106-113, 1998.